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V. B. App
8/4/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Application of :
Jun OSANAI et al. :
Serial No. 09/944,365 : Group Art Unit - 2811
Filed: August 31, 2001 : Examiner - Thien F. Tran
For: COMPLEMENTARY MOS :
SEMICONDUCTOR DEVICE AND :
METHOD OF MANUFACTURING :
THE SAME : Docket No. S004-4393

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MS NON-FEE AMENDMENT
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE

S I R:

In response to the Office Action mailed March 3, 2003, applicants amend their application as follows:

IN THE SPECIFICATION:

Please replace the paragraph beginning at page 1, line 12, with the following rewritten paragraph:

--Fig. 89 is a schematic cross sectional view of a conventional semiconductor device. The semiconductor device is composed of a complementary MOS structure (hereinafter

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